# UNITED STATES PATENT AND TRADEMARK OFFICE

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|---|---|---|---|---|
| 10/084,596 | 02/27/2002 | Dale E. Gulick | 2000.051900/TT4033 | 8995 |

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| EXAMINER |
|---|
| WILLIAMS, JEFFERY L |

| ART UNIT | PAPER NUMBER |
|---|---|
| 2137 | |

DATE MAILED: 08/07/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

| | Application No. | Applicant(s) |
|---|---|---|
| **Office Action Summary** | 10/084,596 | GULICK, DALE E. |
| | Examiner | Art Unit | |
| | Jeffery Williams | 2137 | |

*-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --*

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

1)☒ Responsive to communication(s) filed on <u>21 June 2006</u>.

2a)☐ This action is **FINAL**.    2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

4)☒ Claim(s) <u>1-65</u> is/are pending in the application.

    4a) Of the above claim(s) <u>1-50</u> is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) <u>51-65</u> is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

**Application Papers**

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on <u>27 February 2002</u> is/are: a)☒ accepted or b)☐ objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All   b)☐ Some *  c)☐ None of:

       1.☐ Certified copies of the priority documents have been received.

       2.☐ Certified copies of the priority documents have been received in Application No. _____.

       3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

    * See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1)☒ Notice of References Cited (PTO-892)

2)☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3)☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
    Paper No(s)/Mail Date _____.

4)☐ Interview Summary (PTO-413)
    Paper No(s)/Mail Date. _____ .

5)☐ Notice of Informal Patent Application (PTO-152)

6)☐ Other: _____.

1                                          **DETAILED ACTION**

2

3          This action is in response to the communication filed on 6/21/2006.

4          All objections and rejections not set forth below have been withdrawn.

5          Claims 1 – 65 are pending.

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7

8                        ***Continued Examination Under 37 CFR 1.114***

9

10         A request for continued examination under 37 CFR 1.114, including the fee set

11   forth in 37 CFR 1.17(e), was filed in this application after final rejection.  Since this

12   application is eligible for continued examination under 37 CFR 1.114, and the fee set

13   forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action

14   has been withdrawn pursuant to 37 CFR 1.114.  Applicant's submission filed on 6/21/06

15   has been entered.

16

17

18                                  ***Election/Restrictions***

19

20         Claims 1 – 50 are withdrawn from further consideration pursuant to 37 CFR

21   1.142(b) as being drawn to a nonelected invention, there being no allowable generic or

22   linking claim. Election was made **without** traverse in the reply filed on 9/07/2005.

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3                              *Claim Rejections - 35 USC § 103*

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5          The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

6    obviousness rejections set forth in this Office action:

7          (a) A patent may not be obtained though the invention is not identically disclosed or described as set
8          forth in section 102 of this title, if the differences between the subject matter sought to be patented and
9          the prior art are such that the subject matter as a whole would have been obvious at the time the
10         invention was made to a person having ordinary skill in the art to which said subject matter pertains.
11         Patentability shall not be negatived by the manner in which the invention was made.
12
13
14         **Claims 51, 52, 54 – 57, 59 – 62, 64, and 65 are rejected under 35**

15    **U.S.C. 102(e) as being anticipated by Flyntz, "Multi-Level Secure Computer With**

16    **Token-Based Access Control", U.S. Patent 6,389,542 in view of Angelo, "Method**

17    **and Apparatus for Allowing Access to Secured computer Resources by Utilizing a**

18    **Password and an External Encryption Algorithm", U.S. Patent 5,949,882.**

19

20         Regarding claim 51, Flyntz discloses:

21         *receiving a request for an authentication, at a microcontroller,  requesting*

22    *security data from a security device; receiving the security data from the security device,*

23    *at the microcontroller* (Flyntz, col. 2, lines 52-56; col. 15, lines 5-20, 33-36, 53-55).

24    Flyntz discloses that a user requests authentication by supplying security data to the

25    microcontroller, which in turn processes such security data to evaluate acceptance.

26         *evaluating the security data; and approving the authentication if the security data*

27    *is evaluated as acceptable* (Flyntz, col. 10, lines 33-40).

1      Flyntz discloses a microcontroller, serving to control the connection of the CPU to

2      devices located on system buses (Flyntz, fig. 2; col. 5, line 61 – col. 6, line 25; col. 15,

3      lines 21-32).  The system of Flyntz allows for the provision of power to secure system

4      portions after a positive indication of acceptability has been received (Flyntz, Abstract;

5      col. 1, lines 55-63).  The microcontroller receives a request for authentication via

6      connection to a security device (Flyntz, fig. 2:31).  Flyntz, however, does not disclose

7      the microcontroller as *included in a bridge*.

8      Like Flyntz, Angelo discloses controlling circuitry to implement a secure power up

9      procedure for providing power to system portions on system buses, upon permission for

10     authorized users (Angelo, Abstract; col. 6, lines 44-50; col. 11, lines 17-45).  Angelo

11     specifically discloses that the controlling circuitry used to accomplish this procedure is

12     included in the bridge, thus allowing the system to control the connection of the CPU to

13     devices located on system buses (Angelo, fig. 1-130; col. 5, lines 1-30).  The inclusion

14     of the above mentioned security features within the bridge allows for increased

15     hardware security, as security data may be entered via a secure communication path to

16     the bridge after a request for authentication has been received (Angelo, 2:39-43; 11:64-

17     12:9).

18     It would have been obvious to one of ordinary skill in the art to employ the secure

19     bridge implementation of Angelo for connecting devices on system buses along with the

20     security microcontroller of Flyntz for connecting devices on system buses.  This would

21     have been obvious because one of ordinary skill in the art would have been motivated

22     by the showing of prior art that the above mentioned security features need not be

1    constructed as separate system components, but rather, may be feasibly included

2    within the existing computer system's bridge, thereby allowing the secure connection of

3    the CPU to devices located on buses (Angelo, fig. 2-130; col. 2, lines 39-43; 5:13-26;

4    10:33-54), as well as increased hardware security.

5        The combination of Flyntz and Angelo discloses the request being received from

6    a bus external to the bridge (Flyntz, fig. 2, elem. 31).

7

8        Regarding claim 52, the combination of Flyntz and Angelo discloses:

9        *disapproving the authentication if the security data is evaluated as unacceptable*

10   (Flyntz, col. 2, lines 53-57; col. 10, lines 33-37).

11

12       Regarding claim 53, the combination of Flyntz and Angelo discloses *wherein*

13   *evaluating the security data comprises requesting an indication of acceptability inside*

14   *SMM* (Angelo, Abstract; col. 6, lines 44-50; col. 5: 21-30; col. 11, lines 17-45).

15

16       Regarding claim 54, the combination of Flyntz and Angelo discloses:

17       *wherein requesting security data from a security device comprises requesting the*

18   *security data from the security device over a direct connection between the security*

19   *device and the microcontroller; and wherein receiving the security data from the security*

20   *device, at the microcontroller, comprises receiving the security data from the security*

21   *device over the direct connection to the microcontroller* (Flyntz, fig. 2, elem. 31, 32).

1    The combination of Flyntz and Angelo discloses a direct connection between the

2    security device and the microcontroller.

3

4          Regarding claim 55, the combination of Flyntz and Angelo discloses:

5          *wherein requesting security data from a security device comprises requesting*

6    *biometric data from a biometric device; wherein receiving the security data from the*

7    *security device, at the microcontroller, comprises receiving the biometric data from the*

8    *biometric device, at the microcontroller* (Flyntz, col. 2, lines 52-56; col. 15, lines 5-20,

9    33-36, 53-55; col. 6, lines 36-46).

10         *wherein evaluating the security data comprises evaluating the biometric data;*

11   *and wherein approving the authentication if the security data is evaluated as acceptable*

12   *comprises approving the authentication if the biometric data is evaluated as acceptable*

13   (Flyntz, col. 2, lines 52-56; col. 15, lines 5-20, 33-36, 53-55; col. 6, lines 36-46; col. 10,

14   lines 33-40).

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16         Regarding claims 56 – 65, they are the method steps and method implemented

17   on computer readable medium claims corresponding to the method claims above, and

18   are rejected, at least, for the same reasons.

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1                          *Response to Arguments*

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3          Applicant's arguments with respect to claims 51 - 65 have been considered but

4    are moot in view of the new ground(s) of rejection.

5          ·

6

7                              *Conclusion*

8

9    Claims 51 – 65 are rejected.

10

11         The prior art made of record and not relied upon is considered pertinent to

12   applicant's disclosure:

13         *See Notice of References Cited.*

14

15         A shortened statutory period for reply is set to expire **3** months (not less than 90

16   days) from the mailing date of this communication.

17         Any inquiry concerning this communication or earlier communications from the

18   examiner should be directed to Jeffery Williams whose telephone number is (571) 272-

19   7965.  The examiner can normally be reached on 8:30-5:00.

20         If attempts to reach the examiner by telephone are unsuccessful, the examiner's

21   supervisor, Emmanuel Moise can be reached on (571) 272-3865.  The fax phone

1  number for the organization where this application or proceeding is assigned is (703)

2  872-9306.

3       Information regarding the status of an application may be obtained from the

4  Patent Application Information Retrieval (PAIR) system.  Status information for

5  published applications may be obtained from either Private PAIR or Public PAIR.

6  Status information for unpublished applications is available through Private PAIR only.

7  For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

8  you have questions on access to the Private PAIR system, contact the Electronic

9  Business Center (EBC) at 866-217-9197 (toll-free).

10

11
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